

The ASI451145 (type UGLBE-C/CV/S-1/2) 4-20mA Signal Splitter is a high-precision, DIN rail-mounted unit designed to split and isolate an input current signal into two independent, fully isolated outputs. With high accuracy and ultra-fast response times, it ensures precise signal transmission without interference or loss. Requires a 24V DC External Power Supply for operation.

This industrial-grade signal conditioner is ideal for applications such as process automation, data acquisition, and PLC integration, where multiple devices need to receive the same sensor signal without signal degradation. The compact, space-saving DIN rail mount allows for quick and secure installation in control panels.

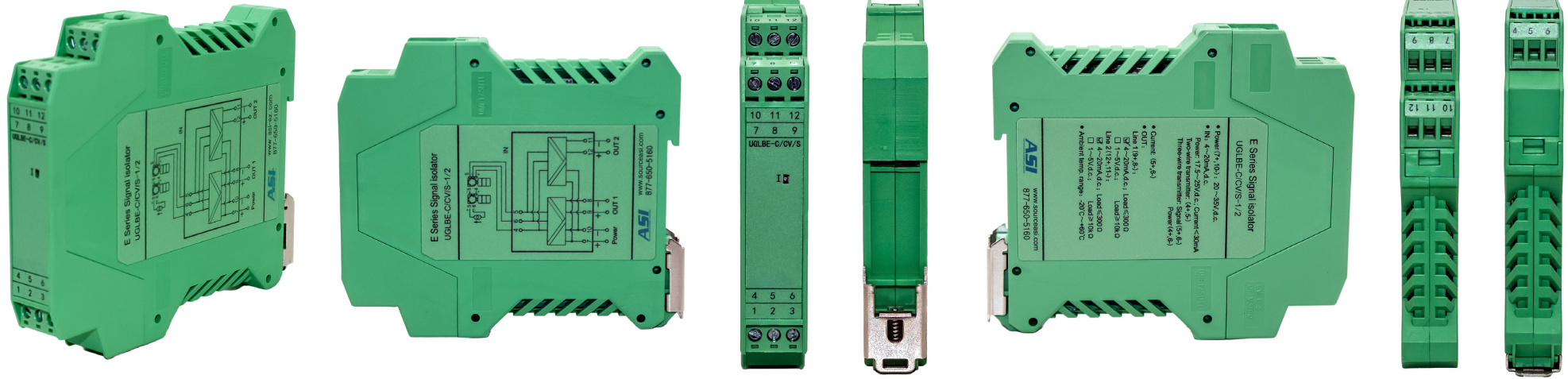
Compared to competitors the ASI451145 offers superior signal accuracy, isolation, and cost-efficiency, making it the best choice for high-performance industrial applications.

Key Features & Benefits:

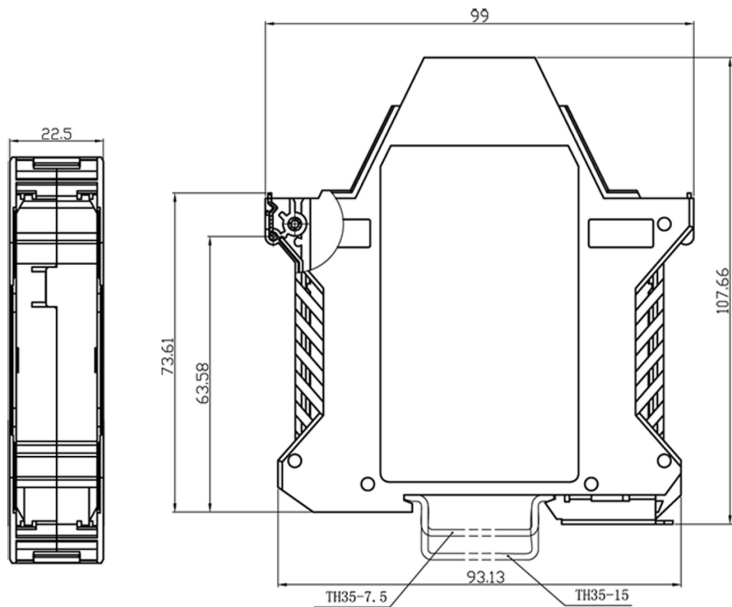
- Input: Single 4-20mA or 0-20mA (2-wire or 3-wire)
- Outputs: Dual independent 4-20mA or 0-20mA
- Isolation: Three-way electrical isolation between input and outputs
- Mounting: Compact design for standard 35mm DIN rail
- Power Supply: Operates on 24V DC nominal

Single 2-wire and 3-wire 4-20mA Input, Dual 4-20mA Output	ASI451145
Dimensions	
Depth/Width/Height	99 x 17.5 x 114 mm
Input	
Input Current	4-20mA or 0-20mA
Voltage	17.5-25 V
Input Impedance	≤ 50 Ω
Maximum Current	≤ 30mAdc
Outputs 1 and 2	
Output Current	4-20mA or 0-20mA
Load Resistance	RL ≥ 300 Ω
Technical Information	
Power Supply	20-30 Vdc
Power Consumption (24V DC Power Supply)	≤ 60mA
Power Dissipation (24V, 20mA)	~ 1.7 w
Output Accuracy (20° C, 4-20mA)	0.1% FS
Temperature Drift (-20° C ~ +60 °C)	0.05% FS/10° C
Response Time	≤ 5mS
Dielectric Strength (Between input, Output and Power)	1500 Vac, 1 min
Insulation Resistance (Between input, Output and Power)	≤ 100M Ω, 500 Vdc
Electromagnetic Compatibility	GB/T 18268 (IEC 61326-1)
Ambient Temperature	-20°C to +60 °C
Wire Size	20-14 AWG
Stripping Length	8mm

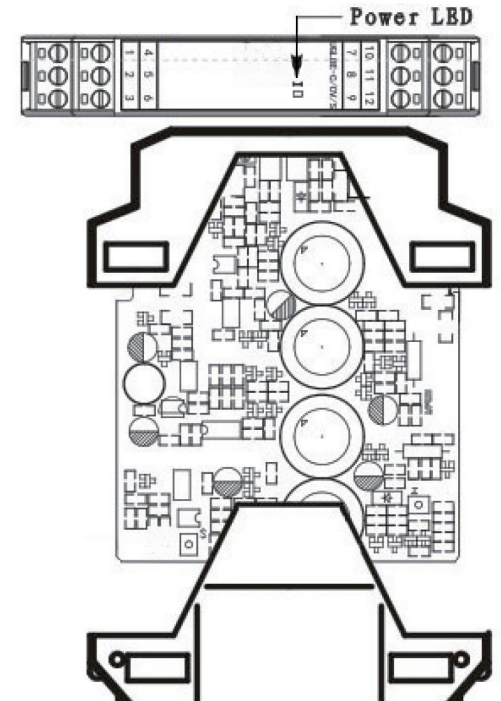
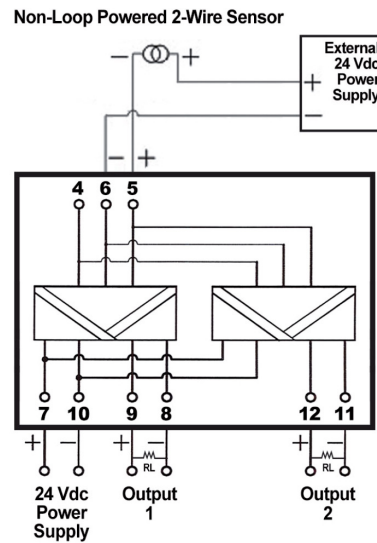
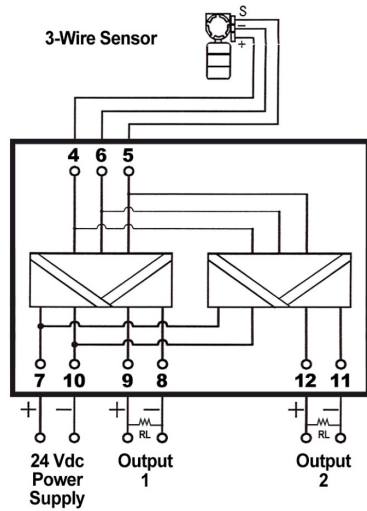
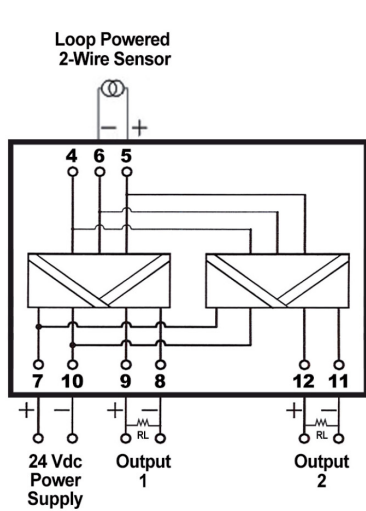
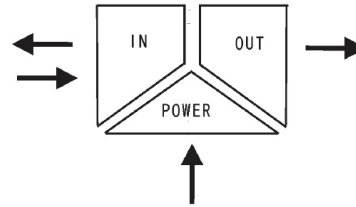
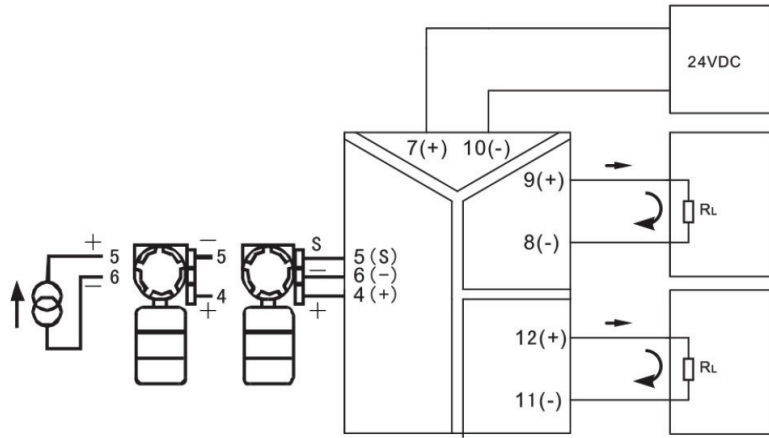
Additional Photos



Drawing



Diagrams



*Users may need to add an optional 250 Ω resistor to clear up the signal.

Diagrams

